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# **A GPU-based Architecture for Real-Time Data**

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## High Speed X-Ray Imaging



Tomography at Synchrotron Light Sources **From 2009 Storage Ring Experiment** alternatively **DMM Slits Detector Slits** ` **CCD** Sample  $\mathcal{L}$ **Bending Magnet** XRy<br>Film Attenuator  $\mathcal{X}$ **Be-window** 

X-ray imaging permits spatially resolved visualization of 2D and 3D structures in materials and organisms which is crucial for understanding their properties. Furthermore, it allows one to recognize defects in devices from the macro- down to the nanoscale. Providing millions of pixels, each with a digitization depth of 12 bits or more, and several thousand frames per second, modern synchrotron can produce data sets of gigabytes in a few seconds. We have developed a high performance imaging station based on NVIDIA GPUs and parallelized the reconstruction software employed at the micro-tomography beamline at KIT and ESRF. Using the built setup, we were able to reduce reconstruction time of typical data-set below one minute.



**Architecture**

— All GPUs are used for reconstruction and all

CPUs are used to preprocess projections

- The data is prefetched from disk while CPUs and GPUs are crunching loaded data
- Both system and GPU memory are allocated once at application startup

#### **Data Transfer**



*The sample, evenly rotating in the front of a pixel detector, is penetrated by Xrays produced in the synchrotron*

*The pixel detector registers series of parallel 2D projections of the sample density at different angles.*

## 3D Image Reconstruction

*Filtered back-projection is used to reconstruct 3D images from a manifold of 2D projections. The projection values are smeared back over 2D cross sections and integrated over all projection angles. To reduce blurring effect the projections are filtered in the Fourier space before being back projected.*

 $\mathsf{computed}\ \mathsf{by} \sum I_p(x\cdot\cos(pa)-y\cdot\sin(pa),z)$  , where  $P$  is the number of projections *p*=1 *P* According to the Back Projection algorithm, the pixel at position *(x,y,z)* is *α* is the angle between projections, and *I<sup>p</sup>* is the image of *p*-th projection.







- Pinned (unswappable) memory buffers are used to exchange the data with GPU
- $-$  The slice is split in blocks and the data transfer of next block is interleaved with computation of current one
- The blocks are still big enough to fully utilize GPU multiprocessors
- **Batched processing is used to filter slices**
- Two real convolutions are computed using a single complex cuFFT transform

Software Optimizations

### Performance Evaluation

*Example for 3D X-Ray imaging. The functional groups of a flightless weevil are colored*



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**Filtering**

— Data is padded to a size equal to the power of 2

#### **Back Projection**

- On GT200 cards, texture engine is used to accelerate random access and linear interpolation
- For Fermi cards, a shared memory is used to reduce number of texture fetches
- To hide memory latencies caused by low occupancy due to high register usage, Fermi kernel processes four pixels per thread



## Fermi-specific Optimizations